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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Shekar)	Examiner: Joseph H NGUYEN
	MALLIKARJUNASWAMY)	
Serial No.:	10/658,181)	Art Unit: 2815
)	
Filed:	September 9, 2003)	Our Ref: B-5716 865040-1
)	
For:	"ESD PROTECTION FOR INTEGRATED CIRCUITS")	Date: January 26, 2006
)	
)	Re: <i>Restriction Requirement</i>
)	

RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the non-final Office Action mailed on November 28, 2005, a response to which is due no later than **May 28, 2006**, Applicant submits this paper together with a petition for a one month extension of time pursuant to 37 C.F.R. 1.136(a) and a check in the amount of \$120.00 for the fee set forth in 37 C.F.R. 1.17(a)(1). Please consider the following remarks, **all of which are made without prejudice.**

Remarks/Arguments begin on page 2 of this paper.

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